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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Sadami TAKEOKA, et al.

Serial No.:

(Continuation of 08/803,145)

Group Art Unit:

Filed: April 30, 2001

Examiner:

For: METHOD OF DESIGNING SEMICONDUCTOR INTEGRATED CIRCUIT

CORRESPONDENCE ADDRESS CHANGECommissioner for Patents and Trademarks
Washington, D. C. 20231

Sir:

Please change the records to indicate the current firm name and telephone number for the above-identified application and forward all future correspondence as follows:

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Respectfully submitted,

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